

Docket No. 259189US40PCT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Motoo ASAI, et al.

SERIAL NO: 10/509,899

GAU: 2633

FILED: June 29, 2005

EXAMINER:

SUBSTRATE FOR MOUNTING IC CHIP, MANUFACTURING METHOD OF SUBSTRATE FOR MOUNTING IC CHIP, DEVICE FOR OPTICAL COMMUNICATION, AND MANUFACTURING METHOD OF DEVICE FOR OPTICAL COMMUNICATION

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references, some of which are cited in the attached Third-Party Observations to the Japanese Patent Office and listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available Unedited Computer-Generated English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s), published application(s) or issued patent(s) which may be related to the present application. In accordance with the waiver of 37 CFR 1.98 dated September 21, 2004, copies of the cited pending applications are not provided. Cited published and/or issued patents, if any, are listed on the attached PTO form 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.
259189US40PCTSERIAL NO.
10/509,899APPLICANT
Motoo ASAI, et al.FILING DATE
June 29, 2005GROUP
2633

LIST OF REFERENCES CITED BY APPLICANT

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	6,788,874 B1	9/7/2004	Shinji ISHIKAWA, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO	2000-321442	11/24/2000	Japan (with English Abstract; corresponding US 6,788,874 B1)		X
	AP	2000-66051	3/3/2000	Japan (with English Abstract and Unedited Computer-Generated English Translation)	X	
	AQ	2000-199827	7/18/2000	Japan (with English Abstract and Unedited Computer-Generated English Translation)	X	
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	Ray T. CHEN, et al., "Fully Embedded Board-Level Guided-Wave Optoelectronic Interconnects", PROCEEDINGS OF THE IEEE, vol. 88, no. 6, June 2000, pages 780-793				
	AX					
	AY					
	AZ					<input type="checkbox"/> Additional References sheet(s) attached

Examiner

Date Considered

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.